

國立清華大學第 26 屆新進人員研究獎得獎人簡介



電機工程學系 彭朋瑞副教授

彭朋瑞於 2008 年自國立台灣大學電機工程學系學士畢業、2010 年自台灣大學信工程學研究所取得碩士學位、並於 2015 年取得國立台灣大學電子工程學博士學位。自 2015 年至 2016 年於國立台灣大學電子研究所擔任博士後研究員。自 2016 年起於元智大學電機工程學系從事教職，後於 2021 年轉任清華大學電機工程學系，現為副教授。他的研究興趣在於高速串列傳收機、鎖相迴路及其應用、高速資料轉換器、以及矽光子介面電路設計。彭博士曾獲得中國電機工程學會優秀青年電機工程師獎、台灣積體電路學會傑出年輕學者獎。他也在 2018 年獲選科技部年輕學者養成計畫得到五年期研究經費並獲頒 Young Scholar Fellowship (2018-2023) 之榮銜。自 2021 年以來，他也擔任 IEEE 亞洲固態電路研討會之技術委員。

Pen-Jui Peng received the B.S. degree in electrical engineering from National Taiwan University (NTU), Taipei, Taiwan, in 2008, the M.S. degree from the Graduate Institute of Communication Engineering, NTU, Taipei, Taiwan, in 2010, and the Ph.D. degree from the Graduate Institute of Electronics Engineering (GIEE), NTU, Taipei, Taiwan, in 2015.

From 2015 to 2016, he was a Post-Doctoral Research Fellow with the GIEE, NTU, Taipei, Taiwan. In 2016, he joined the faculty of the Department of Electrical Engineering at Yuan Ze University, Taoyuan, Taiwan. In 2021, he moved to National Tsing Hua University, Hsinchu, Taiwan, where he is currently an Associate Professor.

His research interests include high-speed SerDes transceivers, phase-locked loops, high-speed data converters, and electrical integrated circuits for silicon photonics.

Dr. Peng has received the Chinese Institute of Electrical Engineering (CIEE) Outstanding Youth Electrical Engineer Award, and the Taiwan IC Design Society (TICD) Outstanding Young Scholar Award. He is a member of IEEE and a life member of Taiwan IC Design Society. He has received the Yong Scholar Fellowship from the Ministry of Science and Technology (MOST) in Taiwan for 5-year research funding (2018–2023). He serves as the Technical Program Committee Member of the IEEE Asian Solid-State Circuits Conference (A-SSCC) since 2021.